Marco Tambussi, Eng.

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Biography

Marco Tambussi was born in Broni (PV), Italy in 1996. He received both Bachelor's degree in Electronics and Computer Science Engineering and Master's degree in Microelectronics Engineering (*summa cum laude*) from University of Pavia, Italy in 2018 and 2021, respectively. From 2021 he is a Ph.D student at the Integrated MicroSystems and Sensors (IMS²) laboratory of the Department of Electrical, Computer and Biomedical Engineering, University of Pavia. His research interest are analog/mixed-signal circuits with focus on low power oversampled A/D converters.

Employment History

2020 – 2021 **System Architect, Intern**
TDK-Invensense Italy SRL, Milan, Italy
Work Activity: Design of an oversampling SAR ADC for Audio Activity Detection.

Education

2021 – · · · **Ph.D. in Microelectronics**
University of Pavia, Italy.
Thesis title: *Design of data converters for audio applications*.

2018 – 2021 **Master's Degree in Electronic Engineering**
University of Pavia, Italy.
Thesis title: *Design exploration of a noise shaping SAR ADC for audio activity detection*.

2015 – 2018 **Bachelor Degree in Electronic and Computer Engineering**
University of Pavia, Italy.
Thesis title: *Design of variable gain amplifiers chain for coherent optic receiver in CMOS 28nm technology*.

2010 – 2015 **High Shool Degree**
Liceo Scientifico Statale "G. Galilei", Voghera (PV), Italy.

Research Publications

Journal Articles

1. A. Gemelli, **M. Tambussi**, S. Fusetto, A. Aprile, E. Moisello, E. Bonizzoni, and P. Malcovati, "Recent trends in structures and interfaces of mems transducers for audio applications: A review," *Micromachines*, vol. 14, no. 4, 2023, ISSN: 2072-666X. DOI: 10.3390/mi14040847.

Conference Proceedings

1. **M. Tambussi**, M. Grassi, E. Bonizzoni, and P. Malcovati, "Trade-offs in active and passive ns-sar adcs architectures for ultra-low power audio activity detection applications," in *2023 18th Conference on Ph.D Research in Microelectronics and Electronics (PRIME)*, 2023, pp. 165–168. DOI: 10.1109/PRIME58259.2023.10161952.

Languages

Italian	Native.
English	Strong reading, writing and speaking competencies.

Skills

Coding	C, MATLAB, LaTeX, Verilog-A, Verilog.
Computer Aided Design	Cadence, KiCad.
Hardware	Computer, Electronic instrumentations, PCB soldering.
Software	Windows, Linux, macOS, Microsoft 365, Adobe Acrobat, Inkscape.
Misc.	Academic research, tutoring, LaTeX typesetting and publishing.

International Scientific Activity

2023 — Reviewer for IEEE PRIME Conference, IEEE MWSCAS Conference.

Teaching Activity

2021 – 2023 — Tutor of the "Electrical Linear Circuits" course at University of Pavia.

Tutor of the "Electronics I" course at University of Pavia.

2017 – 2018 — Tutor of the "Analysis I" course at University of Pavia.

Mentoring Activity

Bachelor's Theses — Alessandro Colombi, "Progettazione del buffer di ingresso di un front-end analogico per microfoni MEMS in tecnologia CMOS 65nm"

Memberships

2023 – · · · — Institute of Electrical and Electronics Engineers (IEEE) Student Member.