

Ali Marzdar

Date of Birth: Jan – 1996
Email: ali.marzdar01@universitadipavia.it

Address: Via Giulotto 12, Pavia, Italy
Mobile: +393446952614

Education

Doctoral Degree **2024 - Present**

Micro- and Nano-Electronics, [University of Pavia](#)

Supervisor: Dr. Marco Grassi

Master's Degree **2018 – 2021**

Electrical Engineering – Electronic Integrated Circuits, [Shahid Beheshti University](#)

Thesis: Design and Simulation of Low DropOut (LDO) Voltage Regulator with Low Power Consumption

Supervisor: Dr. Ali Jalali

Bachelor's Degree **2014 – 2018**

Electrical Engineering – Electronics, [Shahid Beheshti University](#)

Project: Design and Simulation of 1.2V Bandgap Reference at the high-temperature condition
Advisor: Dr. Ali Jalali

Professional Experience

AMECO Group **2021 – 2024**

Title: Electronics Engineer

Objective: Analyze different analog IC features and determine suitable designs for integration into products.

Research Interests

- Low-power analog IC design
- ASIC design
- Data converters
- Power management IC

Skills

- MATLAB & Simulink
- Circuit Design (Cadence & ADS)
- Circuit Analysis
- Programming (C & C++)
- PCB Design (Altium Designer)

Certificate

- MATLAB/Simulink for Analog and Digital Electronics, [Udemy](#) **Oct 2024**

Language Test

Academic IELTS

Jan 2023

- Overall: 6.5
- Speaking: 7
- Listening: 6.5
- Reading: 6.5
- Writing: 6.5